

Attorney's Docket No.: 14580-044001

REMARKS

Reconsideration and allowance of the above referenced application are respectfully requested.

Initially, the indication that claims 3 and 4 indicate allowable subject matter is appreciatively noted. These claims are retained herein. Both claims 3 and 4 have been amended into an independent form.

Claims 1-2 stand rejected under 35 USC 102 as allegedly being anticipated by Seyyedy. With all due respect, this contention is respectfully traversed, and it is respectfully suggested that claims 1 and 2 are completely patentably distinct over the cited prior art.

Specifically, claims 1-2 define a number of elements including sense amplifiers, which, define inter alia, "writing back data into the memory cells and status cells in reversed states following read operations". The rejection alleges that this is shown in Seyyedy column 7 lines 13-56. The rejection states that this anticipates amplifiers writing data back into the memory cells and status cells in reversed states. In fact, however, the reference is quite different. For example, column 7 lines 27-30 recites "a writeback operation must be performed on the memory cell so that the polarization of the memory cell is returned to its pre-read state". The disclosure column 7

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lines 23-27 further describes that when a logical zero is present in the cell, this reversal will lead to loss of data. Therefore, an exact writeback operation may be required. This disclosed feature, that is, exact writeback, it is completely contrary to claim 1 which recites that the writeback must be in the "reversed state".

Therefore, not only does Seyyed not disclose the subject matter which is claimed, it discloses something very different, in fact, the opposite of the reference.

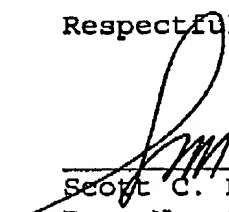
Moreover, column 7 lines 61-64 recites that "it will be understood that the bit line potential can be reversed to change the data on the memory cell and that a zero will be written to the cell when the bit line is low while the plate line is high emphasis added. That is, this system changes the data as required. This is not a writeback of data, but rather a modifying of the data. This is quite contrary to claim 1 which causes writeback in the reversed state. For these reasons, claim 1 should be allowable along with claim to which depends therefrom.

In view of the above amendments and remarks, therefore, all the claims should be in condition for allowance. A formal notice to that effect is respectfully solicited.

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Applicant hereby petitions for a one month extension of time up to and including April 11, 2005. Please apply the Petition for Extension of Time fee and any other charges or credits to Deposit Account No. 06-1050.

Respectfully submitted,

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